

PER UNIT SYSTEM

Per unit system

- It is basically normalization of various voltage levels and various capacity equipments.
- Per unit value of any quantity is defined as :
Actual value in any unit /the base or reference value in the same unit.
- Percent value
- Basic quantities in electrical engg. are
 - (i) Voltage
 - (ii) Current
 - (iii) Impedance
 - (iv) and power

PU System

$$PU\ Value = \frac{Actual\ Quantity}{Base\ Quantity}$$

$$I_{base} = \frac{[VA]_{base}}{[V]_{base}}$$

$$P_{base} = Q_{base} = |S_{base}| = [VA]_{base} = [V]_{base} [I]_{base}$$

$$R_{base} = X_{base} = |Z_{base}| = \frac{[V]_{base}}{[I]_{base}} = \frac{[V]_{base}^2}{S_{base}} = \frac{[V]_{base}^2}{[VA]_{base}}$$

$$Y_{base} = \frac{[I]_{base}}{[V]_{base}}$$

$$|Z|_{PU} = \frac{|Z|_{ohm}}{|Z_{base}|}$$

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Advantages of per unit value system

- By proper specifying base values , the transformer equivalent circuit can be simplified.
Such that voltage currents , external impedances and admittances expressed in per unit do not change when they are referred from one side of a transformer to the others.
- P U quantity is dimensionless.
- Base value is always a real number. There for angle of the per unit value is the same as the angle of actual value.
- Quantities fall in narrow range by using p.u. system

Types of buses

SLACK BUS

- **Slack Bus /swing Bus/ reference bus:** Voltage magnitude and voltage phase angle are specified. Normally voltage magnitude is set to 1 pu and voltage angle is set to zero.
- The real and reactive powers are not specified.
- In load flow study, real and reactive power cannot be fixed at all the buses. As the net complex power flow into the network is not known in advance. So, the system power losses are unknown till the load flow study is complete.

PQ BUS/PV BUS

- A pure load bus is PQ bus. The known variables on bus are real power P_i and reactive power Q_i .
- The unknown variables are V_i and voltage angle δ_i . The PQ buses are the most common comprising almost 85% of all the buses in a given power system.
- A generator is always connected to a PV bus. Hence the net power P_i , and the voltage magnitude V_i are known. PV buses comprise about 15% of all the buses in a power system.

Voltage controlled buses

- Voltage controlled bus has voltage controlled capabilities and uses a tap adjustable transformer. And /or a static VAR compensator instead of generator.

Limits:

- All the state and control variables must be within the specified limits. are called operating constraints.
- Voltage magnitude V_i must satisfy the equality..
- Certain value of the voltage angles δ_i must satisfy the inequality. this constraint limits the maximum permissible power angle of transmission line connecting buses i and k and is imposed by consideration of stability.

LOAD FLOW ANALYSIS

Importance/Significance of Load flow Analysis

- Load flow analysis is the backbone of PSA.
- It is required for Planning, Operation, Economic Scheduling & Exchange of power b/w utilities . Expansion of system & also in design stage.
- Steady-state analysis, of an interconnected PS during normal operating conditions.
- Compute steady-state voltage & voltage angle b/w all buses in n/w.
- Real & Reactive power flow in every Tr. line and transformers under the assumption of known values of generation & load

Methods used for LFA

- Gauss- Seidel Load Flow Method
- Newton- Raphson Load Flow Method
- Fast-Decouple Load Flow Method

Gauss-Seidel Algorithm

1. Form Y bus matrix
2. Assume, $V_k = V_{k(\text{spec})} \angle 0^\circ$ at all generator buses.
3. Assume , $V_k = 1 \angle 0^\circ = 1+j0$ at all load buses.
4. Iteration count setting (iter=1)
5. Let bus number i=1
6. If 'i' refer to generator bus go to step no.7, or else go to step 8
7. a. if 'i' refers to the slack bus go to step 9, or else go to step 7(b).

7. b. Compute Q_i using,

$$Q_i^{\text{cal}} = - \text{Im} \left[\sum_{j=1}^N V_i^* Y_{ij} V_j \right]$$

$$Q_{Gi} = Q_i^{\text{cal}} + Q_{Li}$$

Contd...

Check for Q limit violation.

If $Q_{i(\min)} < Q_{Gi} < Q_{i(\max)}$, then $Q_{i(\text{spec})} = Q_i^{\text{cal}}$

If $Q_{i(\min)} < Q_{Gi}$, then $Q_{i(\text{spec})} = Q_{i(\min)} - Q_{Li}$

If $Q_{i(\max)} < Q_{Gi}$, then $Q_{i(\text{spec})} = Q_{i(\max)} - Q_{Li}$

If Q_{limit} is violated, then treat this bus as P-Q bus till convergence is obtained

8. Compute V_i using the equation,

$$V_i^{\text{new}} = \frac{1}{Y_{ij}} \left[\frac{P_{i(\text{spec})} - Q_{i(\text{spec})}}{V_i^{\text{old}} * } - \sum_{j=1}^{j-1} Y_{ij} V_j^{\text{new}} - \sum_{i=j+1}^n Y_{ij} V_i^{\text{old}} \right]$$

9. If 'i' is less than number of buses, increment i by 1 and go to step 6.

Contd...

10. Compare two successive iteration values for V_i

If $V_i^{\text{new}} - V_i^{\text{old}} < \text{tolerance}$, go to step 12

11. Update new voltages as

$$V_i^{\text{new}} = V_i^{\text{old}} + \alpha (V_i^{\text{new}} - V_i^{\text{old}})$$

$$V_i^{\text{old}} = V_i^{\text{new}}$$

12. Compute relevant quantities

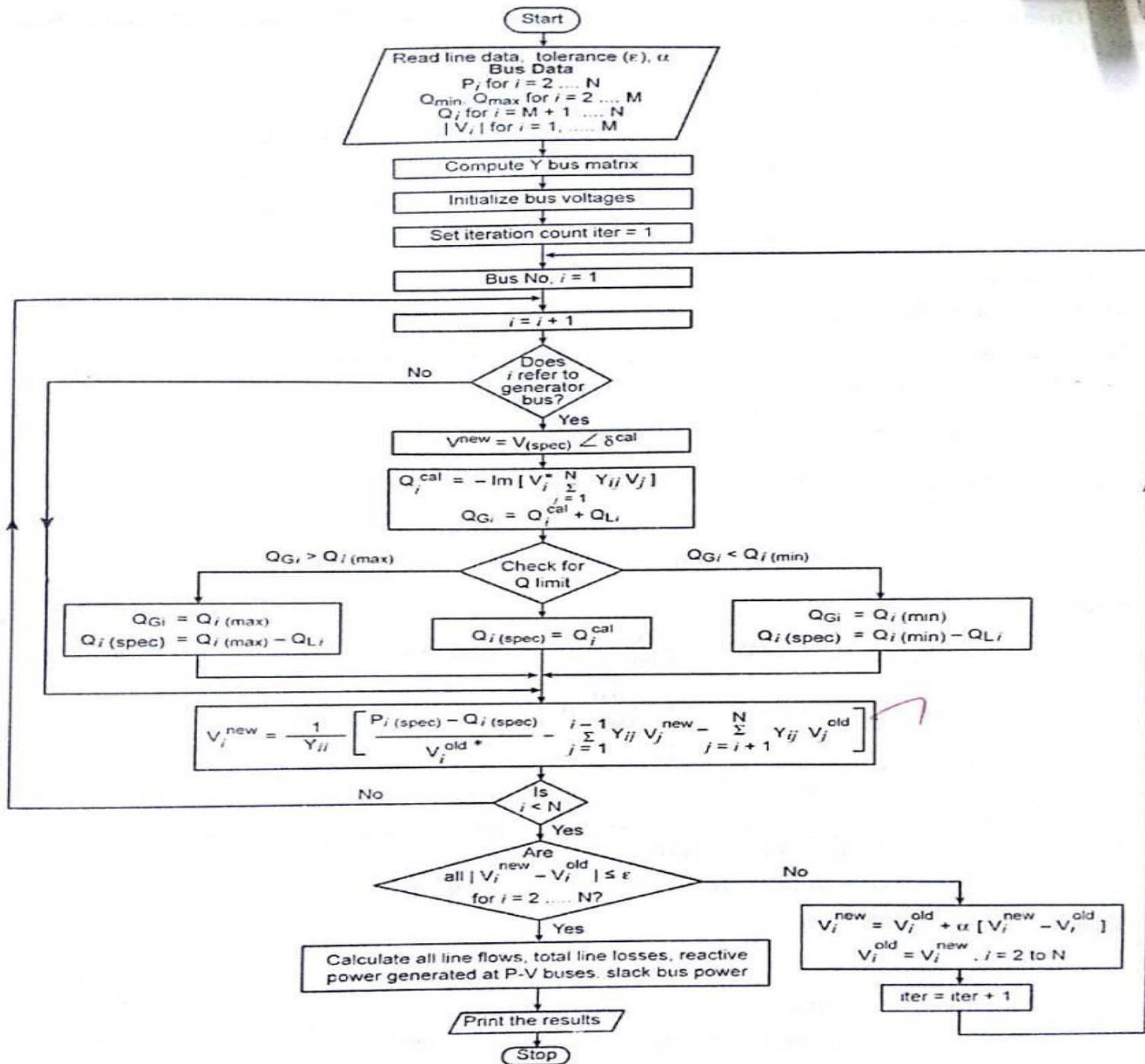
$$\text{Slack bus power, } S_i = P_i - j Q_i = V^* I = V_i^* \left[\sum_{j=1}^N Y_{ij} V_j \right]$$

$$\text{Line flow, } S_{ij} = P_{ij} + j Q_{ij} \\ = V_i [V_i^* - V_j^*] Y_{ij \text{ series}}^* + |V_i|^2 Y_{ij}^*$$

$$P_{\text{Loss}} = P_{ij} + P_{ji}$$

$$Q_{\text{Loss}} = Q_{ij} + Q_{ji}$$

13. Stop the execution.



Advantage of Gauss Seidel Method

- Calculation are simple.
- Programming task is lesser.
- Used for small size system.

Disadvantage of Gauss Seidel Method

- Not suitable for larger systems
- Required more no.of. iterations to reach convergence.
- Convergence time increases with size of the system.

Newton-Raphson Algorithm

1. Form Y-bus matrix

2. Assume flat start for starting voltage solution

$\delta_i^0 = 0$, for $i=1,2,\dots,N$ for all buses except slack bus

$|V_i^0| = 1.0$, for $i=M+1,M+2,\dots,N$ (for all PQ bus.

$|V_i| = |V_i|$ (Spec) , for all PV buses and Slack bus.

3. For load bus, calculate P_i^{cal} and Q_i^{cal}

4. For PV buses, check Q-limit violation .

If $Q_i(\min) < Q_i^{cal} < Q_i(\max)$, the bus acts as P-V bus.

If $Q_i^{cal} > Q_i(\max)$, $Q_i(\text{spec})=Q_i(\min)$

If $Q_i^{cal} < Q_i(\min)$, $Q_i(\text{spec}) = Q_i(\min)$, the P-V bus will act as P-Q bus.

5. Compute mismatch vector using,

$$\Delta P_i = P_{i(\text{spec})} - P_i^{cal}$$

$$\Delta Q_i = Q_{i(\text{spec})} - Q_i^{cal}$$

Contd....

6. Compute $\Delta P_i(\max) = \max |\Delta P_i|$, $i=1,2,\dots,N$ (except Slack bus)

$$\Delta Q_i(\max) = \max |\Delta Q_i|, \quad i=M+1,\dots,N$$

7. Compute Jacobian matrix using,

$$\mathbf{J} = \begin{pmatrix} \frac{\partial \mathbf{P}_i}{\partial \delta} & |\mathbf{V}| \cdot \frac{\partial \mathbf{P}_i}{\partial |\mathbf{V}|} \\ \frac{\partial \mathbf{Q}_i}{\partial \delta} & |\mathbf{V}| \cdot \frac{\partial \mathbf{Q}_i}{\partial |\mathbf{V}|} \end{pmatrix}$$

8. Obtain static correction vector using

$$\begin{pmatrix} \Delta \delta \\ \frac{\Delta \mathbf{V}}{|\mathbf{V}|} \end{pmatrix} = [\mathbf{J}]^{-1} \begin{pmatrix} \Delta \mathbf{P} \\ \Delta \mathbf{Q} \end{pmatrix}$$

Contnd....

9. Update state vector using,

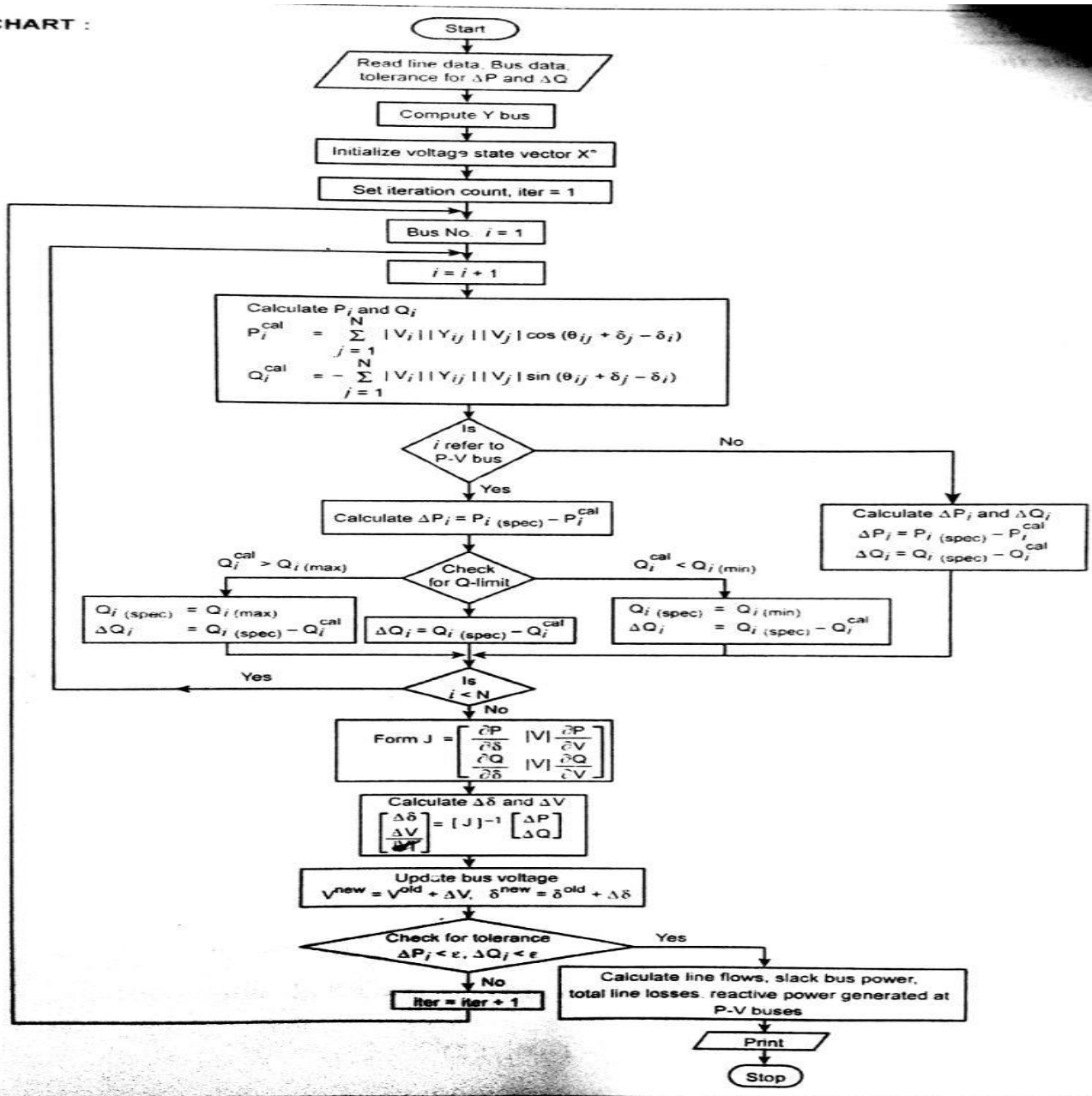
$$\begin{aligned}V^{\text{new}} &= V^{\text{old}} + \Delta V \\ &= V^{\text{old}} + \left[\Delta V / |V^{\text{old}}| \right] \\ &= V^{\text{old}} + \left[1 + \left\{ \Delta V / |V^{\text{old}}| \right\} \right]\end{aligned}$$

$$\delta = \delta^{\text{old}} + \Delta\delta$$

10. This procedure is continued until,

$$|\Delta P_i| < \varepsilon \quad \text{and} \quad |\Delta Q_i| < \varepsilon, \text{ otherwise go to step 3.}$$

DW CHART :



References

- <https://nptel.ac.in/courses/108/104/108104087/>
- <https://nptel.ac.in/courses/108/104/108104051/>
- https://nptel.ac.in › storage2 › nptel_data3 › html › mhrd › ict › text › lec27
- <https://nptel.ac.in/courses/108/106/108106026/>