



<b>BABA BANDA SINGH BAHADUR ENGINEERING COLLEGE, FATEHGARH SAHIB (PUNJAB)</b>		
	<p>Organizes short term Faculty Development Program on</p> <p><b>SOFT COMPUTING TECHNIQUES</b></p> <p>for</p> <p><b>ENGINEERING APPLICATIONS</b></p> <p>Sponsored by <b>MAHARAJA RANJIT SINGH</b> <b>PUNJAB TECHNICAL UNIVERSITY BATHINDA (PUNJAB)</b></p>	
Organized By:	<b>DEPARTMENT OF COMPUTER SCIENCE &amp; ENGG. and IT</b>	<b>July 11-15, 2016</b>

**BABA BANDA SINGH BAHADUR ENGINEERING COLLEGE (BBSBEC), FATEHGARH SAHIB** is going to organize one-week FDP “**Soft Computing Techniques for Engineering Applications**” from 11<sup>th</sup> -15<sup>th</sup> July, 2016. This FDP aims to encourage the participants to get familiar with latest tools, trends, applications and development in the technologies of Soft Computing for engineering applications. FDP will provide a common platform to the faculty members of engineering colleges to focus on exploring various research opportunities and challenges in these latest areas.

***AIM AND OBJECTIVES:***

Soft Computing Techniques have been recognized as attractive alternates to standard hard computing paradigms. Traditional hard computing methods are requiring a precisely stated analytical model and often a lot of computational time. Soft computing techniques (Artificial Neural Network, Genetic Algorithm, Fuzzy Logic) which emphasize gains in understanding behavior in exchange for unnecessary precision, have proved to be important tools for many contemporary problems. The main objective of the FDP is to provide fundamental & practical knowledge in these latest research fields so as to enable participants to solve real world problems and shall be achieved by dividing the work into the following:

1. Cover the basic theory and algorithms that are widely used in Soft Computing
2. Expose faculty members to current technologies & issues that are specific to Soft Computing.
2. Develop hands-on experience covering the installation and configuration, using various simulation tools available in the field of Soft Computing.
3. Develop critical thinking about shortcomings of the state of the Soft Computing.

***ELIGIBILITY OF THE PARTICIPANTS:***

The motivated faculty members of engineering colleges affiliated to MRSPTU and others, who are connected with this upcoming field are eligible to participate in this program. All the participants shall get a participation Certificate; authorized by Maharaja Ranjit Singh Punjab Technical University, Bathinda, provided they attend the full program.

**HOW TO APPLY:**

Interested faculty members can apply to Course Coordinator along with duly filled up **Registration form** on or before **5<sup>th</sup> July, 2016**. Registration form can also be downloaded from the college website [www.bbsbec.edu.in](http://www.bbsbec.edu.in)

Participants are required to **email the scanned copy** of the filled Registration form to the mail-id of the Course Coordinator of FDP ([baljit.singh@bbsbec.ac.in](mailto:baljit.singh@bbsbec.ac.in)), duly forwarded by the Head of the Institution. Such application should be followed by hard copy of the Registration form and the Registration fees.

**EXPERT RESOURCES:**

Eminent speakers from institutions of National repute and Industry have been requested to interact with the delegates during this program.

**TIME & VENUE:**

The one-week program shall commence on **July 11<sup>th</sup>, 2016** and will be spread over two sessions per day. The FDP will be organized in the Seminar Hall of Department of CSE & IT of BBSBEC.

**REGISTRATION FEE:**

A nominal non-refundable registration fee of INR 500 shall be charged from all the participants.

**HOW TO APPLY:**

Interested participants need to apply to the Course Coordinator on or before July 5<sup>th</sup>, 2016. There are limited seats and the rule of first-come first-serve shall apply as applicable. Registration form can also be downloaded from the college website: [www.bbsbec.edu.in](http://www.bbsbec.edu.in) & also from MRSPTU website at: [www.mrsstu.ac.in](http://www.mrsstu.ac.in)

**Participants are required** to send to the mail-id of the Course Coordinator ( [baljit.singh@bbsbec.ac.in](mailto:baljit.singh@bbsbec.ac.in) ), the following, duly forwarded by the concerned Head of the Institution:

1. Scanned copy of the duly filled Registration form
2. Scanned copy of the Registration fee, *in the form of Bank Demand Draft in favor of:*

**Principal, BBSB Engineering College, Fatehgarh Sahib** Payable at 'Fatehgarh Sahib'

**Original copy** of Registration form and Demand Draft (DD) may be submitted at the start of the FDP

<b>IMPORTANT DATA</b>		
Last Date for receipt of application:	<b>July 5, 2016</b>	<b><i>RUSH</i></b> <b>LIMITED ENTRIES: 30 PARTICIPANTS ONLY</b> <b>INCLUDING HOST COLLEGE FACULTY</b>
Registration Confirmation (by mail):	<b>July 6, 2016</b>	

### **Executive Committee:**

<b>Chief Patron:</b>	<b>Professor (Dr) Mohan Paul Singh Ishar</b> Vice Chancellor, Maharaja Ranjit Singh Pb Tech University, Bathinda
<b>Patron:</b>	<b>Maj Gen (Dr) G S Lamba</b> , Principal BBSBEC Fatehgarh Sahib
<b>Co-Patron:</b>	<b>Dr Savina Bansal</b> , DEAN (R&D), Maharaja Ranjit Singh Pb Tech Univ Bathinda
<b>Coordinator:</b>	<b>Dr Baljit Singh Khehra</b> , Head, Deptt. of CSE & IT, BBSBEC Fatehgarh Sahib

### **Organizing Committee:**

Er Preetkamal Sidhu, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Deepak Aggarwal, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Gurleen Kaur Dhaliwal, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Birinder Singh, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Rupinder Kaur Saini, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Balpreet Kaur, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Jatinder Kaur, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Ravdeep Kaur, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Sandeep Kaur Benipal, Asstt Professor, Dept. of CSE, BBSBEC, Fatehgarh Sahib  
Er Simrat Kaur, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Ramandeep Kaur, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib  
Er Ravneet Kaur, Asstt Professor, Dept. of CSE&IT, BBSBEC, Fatehgarh Sahib

### **ADVISORY COMMITTEE:**

Professor (Dr) Savina Bansal, Dean (R&D), Maharaja Ranjit Singh Pb Tech Univ  
Dr Rakesh Kr Bansal, Professor & Dean, Maharaja Ranjit Singh Pb Tech Univ  
Dr Lakhwinder Kaur, Professor & Head, Deptt. of CSE, UCoE, Pbi Univ  
Dr Gurpreet Kaur, Professor & Head, Deptt. of ECE, UCoE, Pbi Univ  
Dr Amar Partap Singh, Professor, Deptt. of ECE, SLIET, Longowal  
Dr Sukhwinder Singh, Professor, Deptt. of CSE, UIET, Pb Univ  
Dr Arvind Kalia, Professor, Deptt. of CS, Himachal Pradesh Univ  
Dr Shakti Kumar, Professor, Dept. of ECE, Baddi Univ  
Dr Maitreyee Dutta, Professor & Head, Dept. of ECE, NITTTR, Chd  
Dr Vikram Singh, Professor, Dept. of CSE, CDLU, Sirsa  
Dr Major Singh, Deptt of CSE, SLIET Longowal

## **ACCOMMODATION FOR OUTSIDE PARTICIPANTS**

Accommodation will be provided to the outstation participants on request & first-come first-serve basis. No TA/DA shall be provided by the organizing College. However participants may claim from their own institutions as applicable

*\*The organizers shall have the right for scrutiny and selection of the candidates based on above mentioned criteria.*

## **ABOUT BBSBEC**

Baba Banda Singh Bahadur Engineering College, Fatehgarh Sahib which is academically affiliated to Maharaja Ranjit Singh Punjab Technical University, Bathinda, has been a premier technical institute of the region. It was established in 1993 by the SGPC and has been contributing to the national kitty, well trained and dedicated young engineers. BBSBEC was the one of the first from Punjab Technical University to get its courses accredited by National Board of Accreditation (NBA) of AICTE. This is a status of quality and repute. Currently, in addition to PG programs leading to the award of the MTech. degree in CSE, CSE(E-Security), ECE, Power Systems, Instrumentation & Control, Mechatronics & Robotics, Machine Design, CAD/CAM, and MBA; the college is also offering UG Programs leading to the award of B.Tech. degree in CSE, IT, ECE, ME, CIVIL, EE and Agriculture. The unparalleled and state-of-the-art infrastructure developed on 90 acres of land & the highly qualified, experienced and dedicated faculty forms the backbone of this institute. More details can be found at: <http://www.bbsbec.edu.in>

## **LOCATION**

Fatehgarh Sahib is a historical place related to the martyrdom of younger sons 'Sahibzadas' of Sri Guru Gobind Singh Ji, the tenth Sikh Guru. It has an ideal geographical location with the proximity to the important cities of the region i.e. Chandigarh, Ludhiana and Patiala. The college is located at 5 kms from Sirhind Railway Station, 42 kms. from Chandigarh, 65 kms from Ludhiana, 40 kms from Roopnagar & 35 kms from Patiala. The nearest Airport is Chandigarh.

**LIMITED ENTRIES: RUSH**

*For any information/clarification, please contact:*

**Dr BALJIT SINGH KHEHRA**

FDP Coordinator &  
Head Deptt of CSE & IT

Cell: + 91 94634 - 46505

electronic mail: [baljit.singh@bbsbec.ac.in](mailto:baljit.singh@bbsbec.ac.in)

## **IMPORTANT DATES**

Last Date for application receipt: **July 5, 2016**

Registration Confirmation (by mail): **July 6, 2016**

Sd/-

**FDP Coordinator:**

**Dr Baljit Singh Khehra**

Head (Department of CSE/IT)

Baba Banda Singh Bahadur Engineering College, Fatehgarh Sahib