* + - What is the function of a microprocessor in a system?
		- Why is the data bus in 8085 bidirectional?
		- How does microprocessor differentiate between data and instruction?
		- Draw the timing diagram of the instruction LDAX B.
		- Sketch and explain the various pins of the 8085.
		- Explain direct addressing mode of 8085 with an example?
		- Draw and explain the timing diagram of the instruction IN 82H.
		- What are the functions of RAM and ROM chips in a microprocessor-based system?
		- How much memory, in terms of bytes, can be interfaced with the 8085? why?
		- Name the registers available in 8255.
		- Write the control word format for the I/O mode of the 8255.
		- Write a brief note on the I/O modes of the 8255.
		- List the internal registers of the 8259.
		- Write a note on cascaded mode of operation in the 8259.
		- Explain the initialization process of the 8259.
		- How is DMA better than programmed data transfer?
		- Give examples of I/O devices that can be interfaced with DMA.
		- Write the sequence of operation carried out in DMA.
* What is the size of address and data bus in the 8086?
* How is the 20-bit physical memory address calculated in the 8086 processor?
* Write the different memory segments used in the 8086 and their functions.
* Write the function of the DF, IF and TF bits in the 8086.
* Explain the architecture of the 8086 with a neat functional block diagram.
* Explain the function of different flags in the 8086.
* What is the function of segment override prefix? Give two examples.
* What is the difference between inter-segment and intra-segment jump in the 8086?
* What is the difference between short and near jump in the 8086?
* What are the different uses of stack in a microprocessor?
* What is the difference between the MUL and IMUL instructions in the 8086?
* What is the difference between the DIV and IDIV instructions in the 8086?
* What is the function of DAA instruction in the 8086?
* Write the operations performed when the instruction AAD is executed in the 8086.
* What is the difference between maskable and non-maskable interrupts?
* What is the difference between hardware and software interrupts?